



20/C
J. Douglas
8/29/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Kidehiko KIRA, et al.**

Group Art Unit: 2652

Serial No.: **09/548,313**

Examiner: **Craig A. Renner**

Filed: **April 12, 2000**

P.T.O. Confirmation No.: **6169**

For: **HEAD ASSEMBLY HAVING INTEGRATED CIRCUIT CHIP COVERED BY LAYER WHICH PREVENTS FOREIGN PARTICLE GENERATION**

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AUG 01 2003

AMENDMENT UNDER 37 CFR §1.111

Technology Center 2600

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

July 29, 2003

Sir:

In response to the Office Action dated January 29, 2003, extended to July 29, 2003 by a three-month Petition for Extension of Time, please amend the above-identified application as follows: